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A method of fabricating a semiconductor structure including Gallium aluminum arsenide integrated circuit Techniques"; 1988 IEEE; GaAs IC Symposium

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**Patent us6693033 - method of removing an amorphous**

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IEEE Transactions on Microwave Theory and Techniques, 54 (5) IEEE 25th annual technical digest of gallium arsenide integrated circuit (GaAs IC) symposium;

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### **Us6630713 - low temperature silicon wafer bond**

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, IEEE Transactions on Microwave Theory and Techniques, , Technical Digest - GaAs IC Symposium (Gallium Arsenide Integrated Circuit) (1988

### **Erl: publications - department of electrical and computer**

8th IEEE International Symposium on Electron Devices for Microwave and Microwave Theory and Techniques, IEEE Microwave Symposium Digest, 1991., IEEE MTT

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### **Patent us5388068 - superconductor-semiconductor**

"Memory-Cell Design in Josephson Technology," IEEE Trans Electron Devices, silicon dioxide and gallium arsenide. Monolithic microwave integrated circuit

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( Technical Digest - GaAs IC Symposium (Gallium Arsenide Integrated Circuit), , IEEE TRANSACTIONS ON ELECTRON DEVICES, v.49,

### **Patent us7169619 - method for fabricating**

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P. R. Herczfeld, J. E. Bowers, Microwave Theory and Techniques, IEEE "Silicon Integrated Circuit 2013, OSA Technical Digest (online) (Optical Society

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